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09/651,821  
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PATENT  
09/651,821

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Melanson

Serial No.: 09/651,821

Filed: 08/30/00

Title: CIRCUITS AND METHODS FOR REDUCING INTERFERENCE FROM  
SWITCHED MODE CIRCUITS

Group Art Unit: 2682

Examiner:

Assistant Commissioner for Patents  
Box Amendment  
Washington, D.C. 20231



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INFORMATION DISCLOSURE STATEMENT

Applicant hereby submits the following references in accordance with 37 C.F.R. §§ 1.56, 1.97 and 1.98. Copies of the references cited in the attached PTO-1449 are enclosed for the convenience of the Examiner. Furthermore, pursuant to 37 C.F.R. § 1.97(g) and (h), no representation is made that this is material to patentability of the present application or that a search has been made.

Applicant hereby submits that claims of Applicant's above-referenced patent application are patentably distinguishable from these references..

Respectfully submitted,  
WINSTEAD SECHREST & MINICK  
Attorneys for Applicant

By: James J. Murphy  
James J. Murphy  
Reg. No. 34,503

5400 Renaissance Tower  
1201 Elm Street  
Dallas, Texas 75270  
(214) 745-5374  
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